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PATENT NUMBER


## U.S. UTILITY Patent Application

**O.I.P.E.**

**PATENT DATE**

SCANNED

Gibber & aa Am

APPLICATION NO. 09/802196	CONT/PRIOR D	CLASS 712	SUBCLASS Z25	ART UNIT 2183	EXAMINER 
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Ashley Saulsbury

TREAT

Processing architecture having an array bounds check capability

PTO-2040  
12/99[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
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